

ABSTRACT

A programmer system and method of programming programmable micro devices with significantly increased throughput are provided. The programmer system includes a multiple number of sockets for programming a multiple number of micro devices simultaneously. A 5 buffer circuit is also provided which is capable of providing a number of logic levels suitable to drive different micro devices during programming by a programmer system adapted to program a number of micro devices simultaneously. A method for programming a programmable micro device is further provided which uses the standard bus cycle from a processor for programming. Finally, a data compare circuit and a method for verifying data 10 programmed by a programmer in a plurality of programmed micro devices using a single read-back operation is also disclosed.